Short CV

Fotios Vartziotis received his Diploma in Electrical Engineering in 1998 from Aristotle University of Thessaloniki (GR), the M.Sc. degree in Telematics (Communications and Software) with Distinction in 2000 from University of Surrey (UK), the MSc degree in Management of Technical Projects in 2010 from Hellenic Open University (GR) and the PhD in Computer Science and Engineering in 2016 from University of Ioannina (GR). His main research interests include VLSI design, testing and reliability, Design for Testability (DfT), test-data compression architectures, power- and thermal- aware test methodologies. From 2010 to 2018, he was Lecturer at the Computer Engineering Department, Technological Educational Institute (TEI) of Epirus, Greece. Today, F. Vartziotis is Assistant Professor at the department of Informatics and Telecommunications, University of Ioannina, Greece. He has ten (10) publications in highly competitive conferences and journals. He has participated in a considerable number of European and Greek research projects proposing, designing and developing advanced portable devices and software platforms. He is a member of the IEEE and the Technical Chamber of Greece.

List of indicative publications

P. Georgiou, F. Vartziotis, X. Kavousianos, K. Chakrabarty Testing 3D-SoCs Using Two-Dimensional Time-Division Multiplexing, IEEE Transactions on Computer-Aided Design of Integrated Circuits and System, vol 37 (2018) p3177-3185.

F. Vartziotis, X. Kavousianos, P. Georgiou, K. Chakrabarty, A Branch-&-Bound Test-Access-Mechanism Optimization Method for Multi-Vdd SoCs, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems vol 36 (2017) p1911-1924.

F. Vartziotis, X. Kavousianos, Critical Path - Oriented & Thermal Aware X-Filling for High Un-modeled Defect Coverage, Proc. of Design, Automation, and Test in Europe conference, (DATE 2017).

F. Vartziotis, X. Kavousianos, K. Chakrabarty, A. Jain, R. Parekhji, Time-Division Multiplexing for Testing DVFS-based SoCs, IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems vol 34 (2015) p668-681.

F. Vartziotis, D. Fotiadis, I. Lagaris, A. Likas, PLACEBO: A computational platform for health professionals and bioengineers, Proc. of IEEE Engineering in Medicine and Biology Society, (EMBS 2003).

List of indicative Projects

01/10/2014 →30/09/2015, Researcher in the framework of the "Thales"* Greek research project "RELIABILITY IMPROVEMENT OF INTEGRATED CIRCUITS AND SYSTEMS IN NANOMETER TECHNOLOGY' as member of the T.E.I. of Epirus, Greece.

01/03/2012 →31/3/2014, Senior Engineer in the framework of the INTERREG GR-IT project "SO.NET.T.O.: SOCIAL NETWORK FOR TOURISM OPERATORS" as member of the T.E.I. of Epirus, Greece.

1/1/2004 - 29/2/2004, Technical Coordinator in the framework of the EU Cluster IST project "Active Environment for Health Promotion and Disease Prevention" – ACTIVE HEALTH" as member of FORTH / BRI.

1/6/2002 - 31/12/2003, Technical Coordinator in the framework of the EU IST project "An Innovative Computational Platform for Solving Differential Equations of Modeling Biomedical Processes – PLACEBO" as member of FORTH / BRI.

1/6/2001 - 31/5/2002, Senior Engineer, Design and testing of the Communication Module in the framework of the EU IST project "A Remotely Monitored Wearable Ultrasound Device for the Monitoring and Acceleration of Bone Healing – USBONE" as member of the Foundation for Research and Technology – Hellas / Biomedical Research Institute (FORTH / BRI).

(*THALES Programme is about "Reinforcement of the interdisciplinary and/or inter-institutional research and innovation with the possibility of attracting high standard researchers from abroad through the implementation of basic and applied excellence research".)